## Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

## **Listing of Claims:**

- 1. (Canceled)
- 2. (Currently Amended) The <u>A</u> system of claim 1 wherein, comprising:

a transmitter in a first network node to generate a sequence of symbols, the sequence of including preamble symbols and a data symbol, a last preamble symbol in the sequence of symbols has having a different waveform than other preamble symbols in the sequence of symbols; and

a receiver in a second network node to receive the sequence of symbols generated by the trasmitter, the receiver including a frame synchronizer logic to perform frame synchronization.

3. (Currently Amended) The A system of claim 1, wherein comprising:

a transmitter in a first network node to generate a sequence of symbols, the sequence of including preamble symbols and a data symbol, a waveform of a last preamble symbol in the sequence of symbols is different than waveforms of other preamble symbols in the sequence of symbols; and

<u>a receiver in a second network node to receive the sequence of symbols generated by the transmitter, the receiver including a frame synchronizer logic to perform frame synchronization.</u>

- 4. (Original) The system of claim 3, wherein the difference between the waveform of the last preamble and the waveforms of other preamble symbols provide a way for the frame synchronizer logic to detect the last preamble symbol.
- 5. (Original) The system of claim 3, wherein the last preamble symbol immediately precedes the data symbol and the frame synchronizer logic detects the data symbol by detecting the last preamble symbol.

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- 6. (Original) The system of claim 3, wherein the frame synchronizer logic obtains the data symbol by taking a Fast Fourier Transform (FFT) of the preamble symbols, conjugating FFT coefficients, and taking an inverse FFT.
- 7. (Original) The system of claim 3, wherein the frame synchronizer logic obtains the data symbol by adding a constant to each carrier phase of the preamble symbols.
  - 8. (Canceled)
  - 9. (Canceled)
  - 10. (Canceled)
- 11. (Currently Amended) The A method of claim 10, further comprising:

  generating a sequence of symbols, the sequence of symbols including preamble symbols
  and data symbol;

using a second waveform to represent a last preamble symbol in the sequence of symbols and a first waveform to represent other preamble symbols in the sequence of symbols, wherein the second waveform is substantially different than the first waveform; and

receiving the sequence of symbols generated by the transmitter, the receiver including a frame synchronizer logic to perform frame synchronization.

- 12. (Original) The method of claim 11, further comprising:
  detecting the last preamble symbol in the sequence of symbols by recognizing the substantial difference between the second waveform and the first waveform.
- 13. (Original) The method of claim 11, further comprising placing the last preamble immediately before the data symbol.
- 14. (Original) The method of claim 11, further comprising detecting the data symbol by recognizing the last preamble symbol.

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- 15. (Original) The method of claim 11, further comprising obtaining the data symbol by adding a constant to each carrier phase of the preamble symbols.
- 16. (Currently Amended) A machine-readable medium comprising instructions which, when executed by a machine, cause the machine to perform operations comprising: generating a sequence of symbols, the sequence of symbols including preamble symbols and a data symbol, a last preamble symbol in the sequence of symbols having a different waveform than other preamble symbols in the sequence of symbols; and

receiving the sequence of symbols generated by the transmitter, the receiver including a frame synchronizer logic to perform frame synchronization.